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TO: United States Patent and Trademark Office, Attn: Art Unit 2128  
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SUBJECT: Amendment and Response for Application 10/061,066

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Patent

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NASA Case No.: NPO-20773-CU

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Inventor: Stoica, Adrian

Serial No.: 10/061,066

Filed: January 29, 2002

For: EVOLUTIONARY TECHNIQUE FOR  
AUTOMATED SYNTHESIS OF  
ELECTRONIC CIRCUITS

Patent Examiner: Luu, Cuong V.

Group Art Unit: 2128

RESPONSE TO OFFICE ACTION

In response to the Official Action dated February 13, 2006, the applicant hereby requests reconsideration and allowance of the application in view of the following.

Claims 1-27 stand rejected under 35 U.S.C. § 101 as being drawn to non-statutory subject matter. Claims 1-27 stand rejected under 35 U.S.C. § 112, second paragraph as indefinite. Claims 15 and 17-20 stand rejected under 35 U.S.C. § 103(a) as unpatentable over Stoica et al., "Evolutionary Design of Electronic Devices and Circuits, Evolutionary Computation" IEEE CEC 99, July 1999 in view of Weste et al, "Principles of CMOS VLSI Design: A System Perspective," Second Edition, 1993, Addison-Wesley Publishing Company. Finally, claims 1, 8, and 21 (along with dependent claims) would be allowable if rewritten to overcome the 35 U.S.C. § 101 and 112, second paragraph rejections noted above.